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DATE: Monday, November 01, 2004

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	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L9	l3 and l8	12
<input type="checkbox"/>	L8	drift region and resurf	309
<input type="checkbox"/>	L7	l6 and l4	0
<input type="checkbox"/>	L6	(gate insulation film or gate stack or gate insulator of gate insulating film) same (perpendicular or perpendicularly)	252
<input type="checkbox"/>	L5	(gate insulation film or gate stack or gate insulator) same (perpendicular or perpendicularly)	464
<input type="checkbox"/>	L4	l1 and l2 and L3	15
<input type="checkbox"/>	L3	drift region same (perpendicular or perpendicularly) same concentration	39
<input type="checkbox"/>	L2	base region same (perpendicular or perpendicularly)	648
<input type="checkbox"/>	L1	(semiconductor or silicon) (wafer or substrate)	446880

END OF SEARCH HISTORY

11/1/04

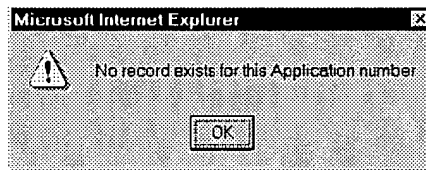
Craig A. Thompson

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US Document Identifiers Search Page

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SEARCH



11-1-04 Craig A. Thompson